

instruction manual revision

GENERAL

This revision outlines changes that have occurred since the printing of your instruction manual. Use this information to correct your manual.

INSTRUCTION MANUALS AFFECTED:

68P81017E85-D	Micor®	Compa-Station Base Radio
		Community Repeater
68P81025E60-F		Base and Repeater Stations
68P81031E95-A		Community Repeater

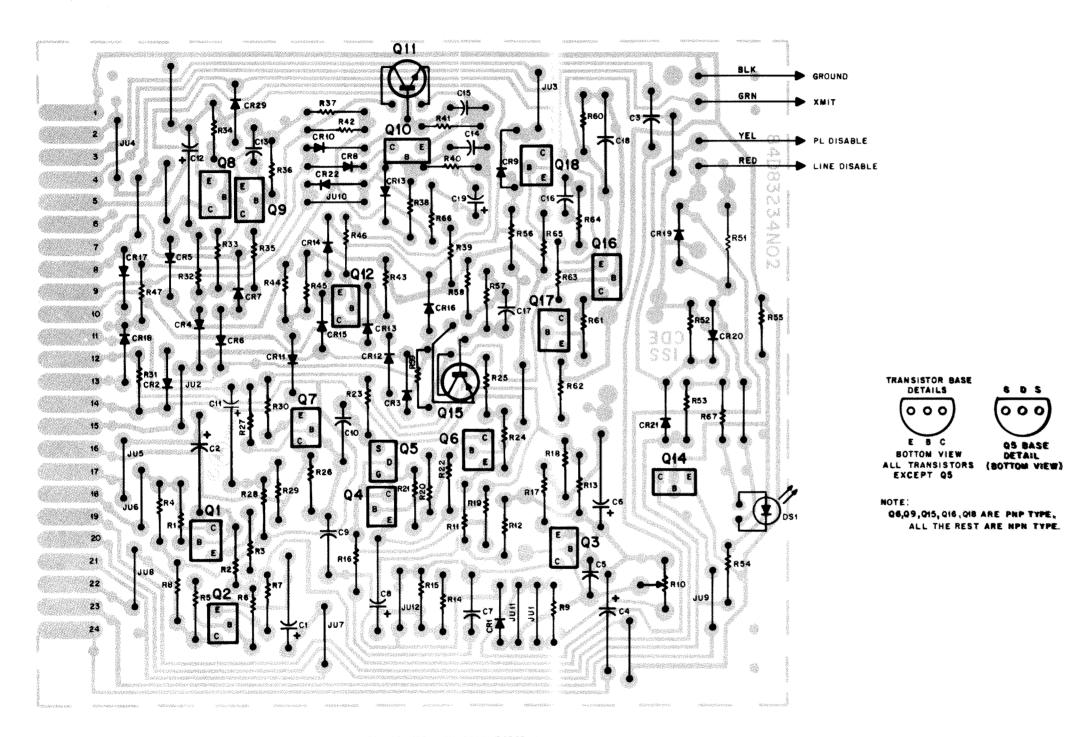
REVISION DETAILS:

The TLN4635C Station Control Module replaces the TLN4635A and TLN4635B. Replace the instruction section in your manual with the attached instruction section 68P81078E27-0.

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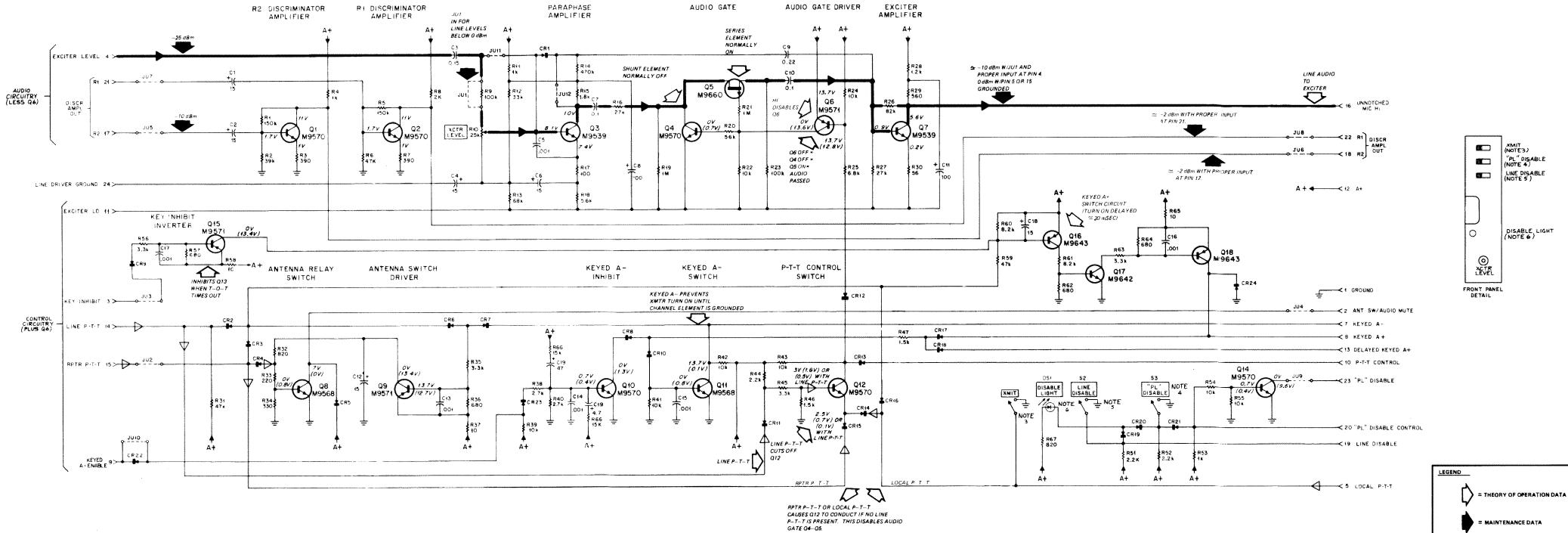
SMR-5536 4/19/88

MODEL TLN4635C STATION CONTROL MODULE



SOLDER SIDE BD-CEPS-34548-A SHOWN FROM SOLDER SIDE

REFERENCE	Control Module MOTOROLA	PL-10952-0	REFERENCE	MOTOROLA	
SYMBOL	PART NO.	DESCRIPTION capacitor, fixed: uF ± 10%; 50 V:unless	SYMBOL	PART NO.	DESCRIPTION
		otherwise stated	R52 R53	6-11009C57 6-11009C49	2.2k 1k
C1,2	23-865136	15 ± 20%; 25 V	R54,55	6-11009C73	10k
C3	8-82905G05	0.15	R56	6-11009C61	3.3k
C4 C5	23-865136 21-82187B29	15 ± 20%; 25 V	R57	6-11009C45	680
C6	23-865136	.001; 100 V 15 ±20%; 25 V	R58	6-11009C01	10
C7	8-82905G07	0.10	R59 R60, 61	6-11009C89 6-11009C71	47k 8.2k
C8	23-84665F03	100	R62	6-11009C45	680
C9	8-82905G11	0.22	R63	6-11009C61	3.3k
C10	8-82905G07	0.10	R64	6-11009C45	680
C11 C12	23-84665F03 23-865136	100 15	R65	6-11009C01	10
C13 thru 17	21-82187829	.001; 100 V	R66 R67	6-11009C77 6-11009C47	15k 820 ohms
C18	23-82783B24	15 ± 10%; 25 V	107	0-11003047	820 011115
C19	23-11019A40	47			switch:
		semidonductor device diode: (see note)	S1 S2,3	40-83468E01 40-83204B01	slide; xmtr. slide; PL & line disable
CR1,2,3,4	48-83654H01	silicon	A CONTRACTOR OF A CONT		hanical parts
CR5 CR6 thru 24	48-82466H13 48-83654H01	silicon silicon	·····	3-84256M01	SCREW, tapping; 2 used
				43-82721C01	BUSHING, snap
001	40 00045000	light emitting diode: (see note)		5-84220B01	GROMMET; 2 used
DS1	48-88245C29	green		9-83697M01	RECEPTACLE, single contact; 24 use
		jumpers:		39-10184A10 64-83415R01	CONTACT, plug; 4 used PANEL, screened
JU1 thru 12	6-11009B23	zero ohm resistor		6-11009B23	JUMPER, zero ohm resistor; 5 used
		transistor: (see note)	note: Replaceme	nt diodes and tran	sistors must be ordered by Motorola p
01,2	48-869642	NPN; type M9642	number only for op	otimum performan	ce.
23	48-869539	NPN; type M9539			
24	48-869642	NPN; type M9642			
25	48-869660	FET, p-channel, type M9660			
26 27	48-869643 48-869539	PNP; type M9643 NPN; type M9539			
28	48-869568	NPN; type M9568			
Q9	48-869643	PNP; type M9643			
Q10	48-869642	NPN; type M9642			
011	48-869568	NPN; type M9568			
Q12 Q13	48-869642	NPN; type M9642 NOT USED			
Q14	48-869642	NPN; type M9642			
Q15, 16	48-869643	PNP; type M9643			
Q17	48-869642	NPN; type M9642			
Q18	48-869643	PNP; type M9643			
		resistor, fixed: ±10%; 1/4 W:			
		unless otherwise stated			
R1 R2	6-11009D02	150k			
72 73	6-11009C87 6-11009C39	39k 390			
R4	6-11009C49	1k			
R5	6-11009D02	150k			
R6	6-11009C89	47k			
77 78	6-11009C39 6-11009C56	390 2k			
10	6-11009C97	2k 100k			
10	18-83083G03	var: 25k			
811	6-11009C49	1k			
112	6-11009C85	33k			
113	6-11009C93	68k			
14 15	6-11009D14 6-11009C55	470k 1.8k			
116	6-11009C83	27k			
17	6-11009C25	100			
118	6-11009C67	5.6k			
19 120	6-11009D22	1 meg			
21	6-11009C91 6-11009D22	56k 1 meg			
22	6-11009C73	10k			
23	6-11009C97	100k			
24	6-11009C73	10k			
25	6-11009C69	6.8k			
126 127	6-11009C95 6-11009C83	82k 27k			
28	6-11009C51	1.2k			
29	6-11009C43	560			
130	6-11009C19	56			
31 32	6-11009C89 6-11009C47	47k 820			
133	6-11009C33	220			
34	6-11009C37	330			
35	6-11009C61	3.3k			
36	6-11009C45	680			
37 38	6-11009C01 6-11009C59	10 2.7k			
39	6-11009C59	2.7K 10k			
40	6-11009C59	2.7k			
41	6-11009C73	10k			
42	6-11009C73	10k			
43	6-11009C73	10k			
44 45	6-11009C57 6-11009C61	2.2k			
45 46	6-11009C61 6-11009C53	3.3k 1.5k			
	6 11009C53	1.5k			
		NOT USED			
148,49,50 151	6-11009C57	2.2k			



MODEL TLN4635C STATION CONTROL MODULE

Maintenance & Troubleshooting

This module may be serviced either while connected to the unified chassis interconnect board or while connected to separate external test equipment. Refer to the unified chassis interconnect board servicing information in this manual for "setup" details.

Step 1. Check jumpers as applicable for the mode of operation of this module.

Step 2. Connect power and signal sources to the module as indicated in the following chart.

n No.	Connect
11, 24	Ground
	Audio Oscillator
	+ 12 Volts dc
	AC Voltmeter to Ground
	10 Kilohms to 12 Volts dc
	10 Kilohms to 12 Volts dc
	10 Kilohms to 12 Volts dc

Note: Level adjust control should be full clockwise.

Step 3. Adjust audio oscillator output for -25 dBm at pin 4. Pin 16 should measure approximately -10 dBm with JU1 connected. If this level cannot be achieved, check stages Q3 and Q7. If the level is correct, ground pin 5 or pin 15 and note that the reading drops to 0. If this does not occur check stages Q4 and Q5 and their associated driver stages

Step 4. Ground pins 14 and 9. Measure the dc voltage at pins 10 and 8. Eachshould read + 12 volts. Pins 7 and 2 should read zero. If a voltage or ground does not appear at the prescribed location, check each stage associated with that location.

Step 5. Ground pin 15. Measure the dc voltage at pins 7 and 2. Each should read + 12 volts. Pin 10 should read zero volts.

Step 6. With pin 15 still grounded, apply a ground to pin 14. Check for + 12 volts do at pin 10

Step 7. Ground pin 20 and check the dc voltage at pin 23. The meter indication should be + 12 volts. Remove the ground from pin 20 and the voltage should drop to zero

Step 8. Apply a -10 dBm signal from the audio oscillator to pin 17 and measure the ac voltage at pin 18. The voltmeter should indicate approximately -2 dBm.

Step 9. Apply a 10 dBm signal from the audio oscillator to pin 21 and check the ac voltage on pin 22. The indication should be approximately -2 dBm.

Control Theory

= PRIMARY SIGNAL FLOW

EEPS-45663-0

When a PTT signal is applied to pin 5, 14, or 15 the following functions occur:

- A low is applied to the base of Q16. After a 30 millisecond delay. This provides a high output to pin 8 and to Q11 from Q17 and Q18.

- The drive to Q11 will be inhibited by Q10 until a low is applied to pin 9, indicating an oscillator channel element ground. This prevents A- from energizing the transmit-ter circuits until after the channel element has been grounded. Q11 can also be inhibited by a low entering on pin 3 from the time-out-timer module at the end of a pre-set time limit.

 The low is also applied to the base of Q9 where it is inverted and applied as a high to the base of Q8. If a low is applied as repeater PTT on pin 15, Q8 will be inhibited. However, if the low is applied to either pin 5 or 14, Q8 will saturate and provide a low to operate the antenna switch. Switch Q8 does not turn off the instant PTT low is removed. Instead it is kept on for the time required for C12 to discharge through R32 and R33. This allows the high level rf energy to decay before the antenna switch reverts to the receive condition.

 If the PTT low is applied to the module on pin 5 or 15, a conduction path is
provided for Q12. When Q12 conducts, a low is applied to pin 10. This control can be overridden by a line PTT signal applied to pin 14. This signal reaches the base of Q12 causing it to cut off and remove the low from pin 10.

When Xmit switch \$1 is actuated, a ground is supplied to the emitter of Q12 with the same result as a low applied to pin 5 or 15. Actuating line disable switch S2 applies a ground output to pin 19. S2 also provides a ground to the disable light DS1, which causes it to illuminate. When PL disable switch S3 is actuated, DS1 also illuminates and a low is applied to the base of PL disable inverter Q14. This low causes Q14 (which is normally conducting) to cut off and removes the PL disable switched ground from pin 23. The station should not be left in the line or PL disable mode under normal operating conditions.

In Private-Line applications, keyed A- release is delayed at the end of a transmission by an input to pin 13 from the external Private-Line reverse burst circuitry. This input maintains transmitter keying for the duration of the reverse burst tone.

END OF DOCUMENT

FUNCTION

- Integrates control functions from other modules to key the station transmitter.
- Adjusts exciter audio level.
- Amplifies receiver discriminator signals which are used externally.

NOTES

- 1. JU1 is in for line levels below 0 dBm and removed for line levels above 0 dBm.
- 2. Voltages shown in parentheses are normally measured when function is activated. Voltages not in parentheses are normally measured when function is deactivated.
- 3. To key the transmitter, slide the Xmit switch to the right (closed) and hold in this position. To unkey the transmitter, release the switch.
- When the PL disable switch is in the (normal) position (to the left) the Private-Line function of the station is operational. In the actuated position (to the right), the receiver Private-Line tone-coded squelch circuit is disabled so that all onfrequency signals may be monitored.
- When the line disable switch is in the normal position (to the left, open), station operation can be initiated by remote control in the actuated position (to the right, closed), remote controls are disabled and the station can only be operated via local controls.
- 6. The disable light is illuminated when either the PL or line disable switches are actuated.

Jumper	JU1	JU2	JU3	JU4	JU5	JU6
Non-Trunked Repeater	IN	IN	IN	OUT	IN	IN
Trunked Repeater (SECURENET Capable)	IN	IN	IN	OUT	OUT	Ουτ
Trunked Repeater Earlier "AT" Version	IN	IN	IN	OUT	IN	IN

Jumper	JU7	JUB	eut	JU10	JU11	JU12
Non-Trunked Repeater	IN	1N	IN	IN	ОИТ	OUT
Trunked Repeater (SECURENET Capable)	OUT	OUT	IN	IN	OUT	OUT
Trunked Repeater Earlier "AT" Version	IN	IN	IN	IN	OUT	OUT