

ENHANCED FRONT END INSTRUCTION MANUAL

FE3H 29 - 50 MHz

Covers Models:		
A10-FE3H035	A10-FE3H045	

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GENERAL INFORMATION

INTRODUCTION

The FE3H040 Enhanced Front End is a highly integrated, frequency selective, down converter used in Daniels Electronics standard MT-3 Low Band VHF FM receiver product line. The FE3H040 Front End connects to the 21.4 MHz IF / Audio Main Board and the enhanced Synthesizer to form a complete receiver..

PERFORMANCE SPECIFICATIONS

Frequency Range:	29.0 MHz - 38.5 MHz (FE3H035) 37.5 MHz - 50.0 MHz (FE3H045)		
IF Frequency:	21.4 MHz		
3 dB Bandwidth:	> 5 MHz (Preselector Filter)		
Conversion Gain:	12.5 dB		
Third Order Intercept Point:	> +18 dBm		
Image Rejection:	> 100 dB		
Impedance:	50 Ω at RF, LO, and IF ports		
Temperature Range:	-30°C To +60°C (Optional -40°C To +60°C)		
Power Requirements: +9.5 Vdc @ 160 mA			



THEORY OF OPERATION

GENERAL

The Enhanced Front End amplifies and down converts the RF signal to the IF frequency of 21.4 MHz. The FE3H035 and FE3H045 Front Ends use high side injection. Power is supplied by a regulated +9.5 volts and draws typically 160 mA. The Enhanced Front End consists of the following:

- Five Section L-C coupled Bandpass Filter
- · Single Stage Low Noise RF Amplifier
- Image Rejection Filter
- · Local Oscillator Low Pass Filter
- Double Balanced Active Mixer

All terminations are 50 Ω impedance.



POWER AND INTERCONNECTION

The FE3H Enhanced Front End operates from +9.5 Vdc applied via a short red wire connected to supply point J6-2 on the receiver main IF/ audio board. Ground return is supplied via direct mounting to the receiver chassis and coaxial cable interconnections. The FE3H Front End draws approximately 160 mA from the IF/audio board's +9.5 Vdc supply.

The RF input signal connected to the receiver front panel type N connector is fed to the Front End SMB input connector (J1) by a short coaxial cable. The FE3H Front End's local oscillator input and IF output are respectively routed by coaxial cable from the local oscillator module and to SMB connector J7 on the main IF/audio board.

PRESELECTOR FILTER

The received RF signal is filtered by a five section L-C coupled band pass filter. This filter provides front end selectivity and attenuates any potential internally generated spurious signals. The filter has an operational bandwidth of 5 MHz and a typical insertion loss of 2 dB. The filter is tunable (L1 through L5) in the following ranges:

• FE3H035	29.0 MHz - 38.5 MHz
• FE3H045	37.5 MHz - 50.0 MHz

Every Enhanced Front End has an identification label indicating one of two operating frequency bands.

RF AMPLIFIER

The output from the preselector filter is coupled through DC blocking capacitor C8 to the base of RF amplifier Q2. The RF amplifier stage is supplied by the regulated +9.5 Vdc line and typically draws 60 mA of collector current. It provides a high third order intercept point and low noise figure. Transistor Q1 actively regulates the bias to RF transistor Q2. The output signal is coupled to an image rejection filter via DC blocking capacitor C10. Typically, the RF amplifier stage has a gain of 16 dB.

IMAGE REJECTION FILTER

The amplified RF signal is followed by an image rejection filter. This filter rejects image noise after amplification. The low pass filter consists of C11 through C14, and L3 through L5. It is a low pass filter designed to pass the desired frequency range of 29.0 MHz - 50.0 MHz and reject the image band of 71.8 MHz - 92.8 MHz. Typical insertion loss is 1.5 dB.

LOCAL OSCILLATOR FILTER

The local oscillator filter is a low pass filter consisting of C34 through C37, and L10 through L12. Typical insertion loss is 1.5 dB. Input level to the local oscillator filter is typically +5 dBm. Following the filter is an attenuation network with an insertion loss of 7.5 dB.

DOUBLE BALANCED MIXER

The Double Balanced Mixer consists of active mixer M1. The received RF signal in the 29.0 MHz - 50.0 MHz range is down converted to the 21.4 MHz first conversion IF frequency by the use of high side injection. The mixer is driven by an LO signal of typically -4 dBm. Typical current draw for the mixer is 100 mA. Typical conversion loss is 0 dB.



ENHANCED FRONT END ALIGNMENT

REPAIR NOTE

The Enhanced Front End module is mainly made up of surface mount devices that should not be removed or replaced using an ordinary soldering iron. Removal and replacement of surface mount components should be performed only with specifically designed surface mount rework and repair stations complete with Electrostatic Discharge (ESD) protection.

When removing Surface Mount Solder Jumpers, it is recommended to use solder braid in place of manual vacuum type desoldering tools. This will help prevent damage to the circuit boards.

GENERAL

Alignment for the FE3H040 Enhanced Front End consists of tuning the five section bandpass filter only. No tuning is required for frequency changes within the 3 dB bandwidth (5 MHz) of its originally tuned frequency. However, without tuning at the 3 dB band edges, the receivers sensitivity will be degraded. Normally, no tuning is required for small frequency changes. For Large frequency changes use the tuning procedure below.

RECOMMENDED TEST EQUIPMENT

Alignment of the Enhanced Front End requires the following test equipment or its equivalent:

Power supply	Regulated +9.5 Vdc at 1 A. Phillips PM 2811	
Spectrum Analyzer with Tracking Generator		
Alignment Tool	Coilcraft 37-1409	

CHECK POINTS

Vsupply	+ 9.5 Vdc @ 160 mA
Bandpass Filter Loss	2 dB
RF Amplifier Gain	16 dB
Image Rejection Filter Loss	1.5 dB
First Mixer Conversion Loss	0 dB
Vb of Q1	+ 7 Vdc
Vc of Q2	+ 7.5 Vdc
Vp of M1	+ 9.3 Vdc
Local Oscillator Frequency	RF Frequency + 21.4 MHz
Local Oscillator Filter Loss	1.5 dB
Local Oscillator Attenuator Loss	7.5 dB

PREFERRED TUNING PROCEDURE

The preferred method of tuning the Enhanced Front End is to use a Spectrum Analyzer with a Tracking Generator. With the Local Oscillator absent, the frequency response of the Preselector Filter can be seen at the IF port. Use the following procedure for swept frequency tuning:

- Supply +9.5 Vdc to the Enhanced Front End.
- Connect the Tracking Generator output with -10 dBm or less to the RF input.
- Connect the IF output to the Spectrum Analyzer input. Center the spectrum analyzer at the desired RF frequency. Adjust L1 through L5 for a uniformly flat response at a level typically -30 dBc, centered at the desired RF frequency (Refer to Enhanced Front End Tuning Diagram).

ALTERNATE TUNING PROCEDURE

The alternate method of tuning the Enhanced Front End is to use a complete receiver and monitor receiver SINAD. Use the following procedure for tuning:

- Inject the desired RF signal to the RF IN connector at a level of -20 dBm.
- Inject the desired LO signal to the LO IN cable at a level of +5 dBm (LO freq.=RF+21.4 MHz).
- Connect the IF OUT to the IF/RF IN connector on the IF/Audio Board. Adjust L1 through L5 and tune for best receiver SINAD (> -118 dBm).

ENHANCED FRONT END REMOVAL AND INSTALLATION

The Enhanced Front End is fastened to the main board (MT-3 Receiver IF/Audio Board) with a single Phillips screw and fastened to the front panel with two countersunk Phillips screws. Remove these screws and disconnect the IF, LO, and power cables to remove the Enhanced Front End. Installation of the Enhanced Front End is the reverse of the above procedure.

CIRCUIT BOARD REMOVAL

Removal of the circuit boards is accomplished by removing all 18 Phillips screws and lockwashers. To separate the two circuit boards from each other, the small piece of semi-rigid coax must be desoldered. Follow a reverse procedure to re-assemble.





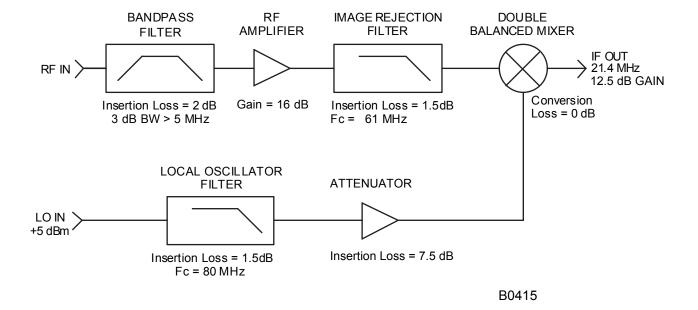
ILLUSTRATIONS AND SCHEMATICS

PRINTED CIRCUIT BOARD NUMBERING CONVENTION

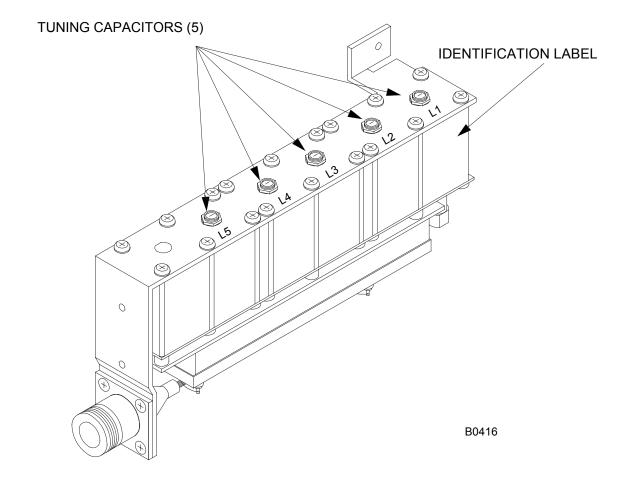
Daniels Electronics Ltd. has adopted a printed circuit board (PCB) numbering convention in which the last two digits of the circuit board number represent the circuit board version. All PCB's manufactured by Daniels Electronics Ltd. are identified by one of the following numbering conventions:

PCB number	43-9120 <u>10</u> Indicates circuit board version 1.0
PCB number	50002- <u>02</u> Indicates circuit board version 2 (no decimal version)

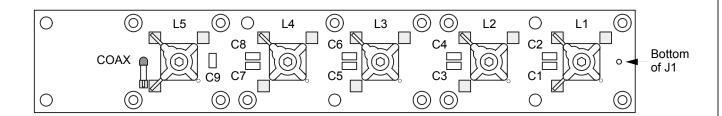
ENHANCED FRONT END BLOCK DIAGRAM



ENHANCED FRONT END TUNING DIAGRAM

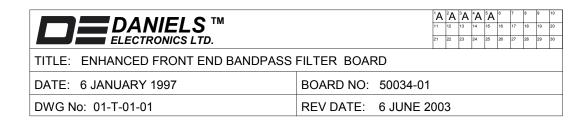


BANDPASS FILTER COMPONENT LAYOUT

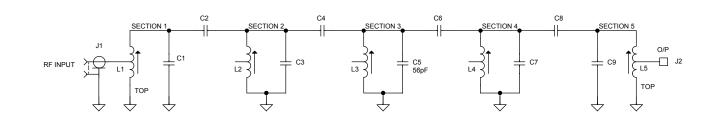


	FE3H035	FE3H045
Designator	29.0 - 38.5 MHz	37.5 - 50.0 MHz
C1	82 pF	68 pF
C2	15 pF	10 pF
C3	68 pF	56 pF
C4	12 pF	8.2 pF
C5	82 pF	68 pF
C6	12 pF	8.2 pF
C7	68 pF	56 pF
C8	15 pF	10 pF
C9	82 pF	68 pF

Designator	Low Band (Blue)	High Band (Green)	
L1	193-306 nH (Tapped)	162-252 nH (Tapped)	
L2	193-306 nH	162-252 nH	
L3	193-306 nH	162-252 nH	
L4	193-306 nH	162-252 nH	
L5	193-306 nH (Tapped)	162-252 nH (Tapped)	



BANDPASS FILTER SCHEMATIC DIAGRAM

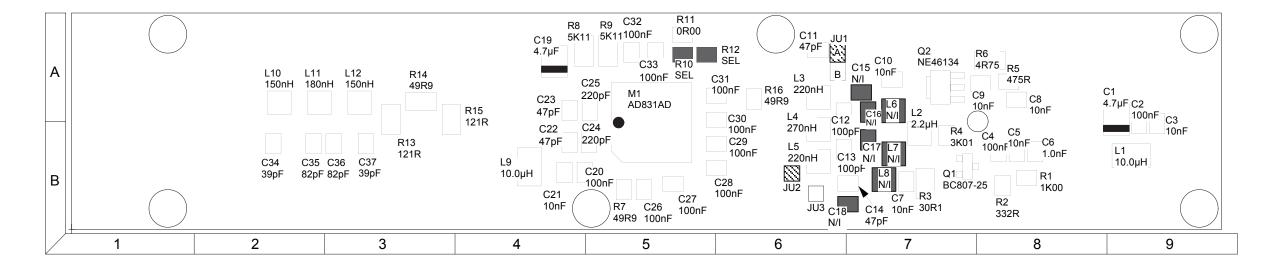


Designator	FE3H035 29-38.5 MHz	FE3H045 37.5-50 MHz	
C1	82 pF	68 pF	
C2	15 pF	10 pF	
C3	86 pF	56 pF	
C4	12 pF	8.2 pF	
C5	82 pF	68 pF	
C6	12 pF	8.2 pF	
C7	68 pF	56 pF	
C8	15 pF	10 pF	
C9	82 pF	68 pF	
L1	193-306 nH	162-252 nH	
L2	193-306 nH	162-252 nH	
L3	193-306 nH	162-252 nH	
L4	193-306 nH	162-252 nH	
L5	193-306 nH	162-252 nH	

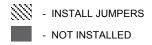
HIGHEST REFERENCE DESIGNATORS						
C9 L5						
UNUSED REFERENCE DESIGNATORS						

1		□ ■ DANIELS™						
ELECTRONICS LTD VICTORIA						VICTORIA BC.		
	TITLE: ENHANCED FRONT END BANDPASS FILTER BOARD SCHEMATIC DIAGRAM					RD SCHEMATIC DIAGRAM		
-		DATE:	6 FEBRUARY 1997	DWN BY	: ROBI	ERT FICHTNER	APRVD:	
1	DWG No.: 01-S-01-01 REVISED BY: NIKKI BEREKOFF				NIKKI BEREKOFF			
1		BOARD No	.: 50034-01	REV: 0)1	DWG REV DATE:	9 JUNE 2003	

ENHANCED FRONT END COMPONENT LAYOUT

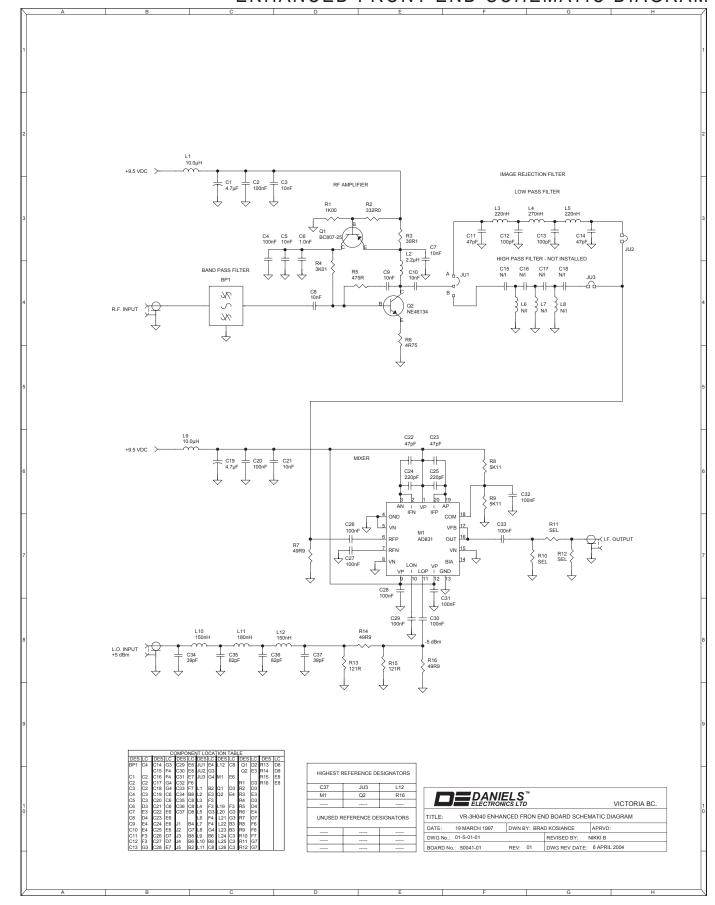


COMPONENT LOCATION TABLE									
DES	LC	DES	LC	DES	LC	DES	LC		
C1 C2 C3 C4 C5 C6 C7 C8 C9 C10 C11 C12 C13 C14 C15 C16 C17 C19 C20	A9 B9 B8 B8 B8 B8 B7 A8 A7 A6 A6 A6 B6 B7 A7 A7 B7 B7 B4 B4	C21 C22 C23 C24 C25 C26 C27 C28 C29 C30 C31 C32 C33 C34 C35 C36 C37	B4 B4 B5 B5 B6 B6 A6 A5 B2 B3 B3 A6 B6	JU3 L1 L2 L3 L4 L5 L6 L7 L8 L9 L10 L11 L12 M1	B6 B9 B7 A6 B6 B6 A7 B7 B7 B4 A2 A2 A3 A5	R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13 R14 R15 R16	B8 B7 A8 A6 B5 A5 A5 A5 A3 A3 A6		



DANIELO IN		¹A	²A	³A	. ⁴A	5	βA	7	8	9	10
DEDANIELS TM ELECTRONICS LTD.				13	14	15 25	16 26	17	18 28	19	30
TITLE: VR-3H040 ENHANCED FRONT END BOARD TOP											
DATE: 25 MARCH 1997 BOARD NO: 50041-01											
DWG No: 01-T-01-01 REV DATE: 5 APRIL 2004											

ENHANCED FRONT END SCHEMATIC DIAGRAM







PARTS LISTS

ENHANCED FRONT END BOARD ELECTRICAL PARTS LIST

Ref		
Desig	Description	Part Number
C1	CAP., SM, 4.7uF TANT., 10%,16V	1055-5B475K16
C2	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C3	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C4	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C5	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C6	CAP., SM, 1nF CER,0805,X7R,50V	1008-3A102K5R
C7	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C8	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C9	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C10	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C11	CAP., SM, 47pF CER., 0805, C0G	1008-1A470J1G
C12	CAP., SM, 100pF CER., 0805,C0G	1008-2A101J1G
C13	CAP., SM, 100pF CER., 0805,C0G	1008-2A101J1G
C14	CAP., SM, 47pF CER., 0805, C0G	1008-1A470J1G
C19	CAP., SM, 4.7uF TANT., 10%,16V	1055-5B475K16
C20	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C21	CAP., SM,10nF CER,0805,X7R,50V	1008-4A103K5R
C22	CAP., SM, 47pF CER., 0805, C0G	1008-1A470J1G
C23	CAP., SM, 47pF CER., 0805, C0G	1008-1A470J1G
C24	CAP., SM, 220pF CER., 0805,C0G	1008-2A221J1G
C25	CAP., SM, 220pF CER., 0805,C0G	1008-2A221J1G
C26	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C27	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C29	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C30	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C31	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C32	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C33	CAP., SM,100nF CER,0805,X7R,50	1008-5A104K5R
C34	CAP., SM, 39pF CER., 0805, C0G	1008-1A390J1G
C35	CAP., SM, 82pF CER., 0805, C0G	1008-1A820J1G
C36	CAP., SM, 82pF CER., 0805, C0G	1008-1A820J1G
C37	CAP., SM, 39pF CER., 0805, C0G	1008-1A390J1G
<u>L1</u>	INDUCTOR, SM, 10.0uH, 10%,1812	1255-4G10000K
L2	INDUCTOR,SM,2.2uH CER,10%,1008	1256-3B2R200K
L3	INDUCTOR,SM,220nH CER,10%,1008	1256-2BR2200K
<u>L4</u>	INDUCTOR,SM,270nH CER,10%,1008	1256-2BR2700K



Enhanced Front End Board Electrical Parts List cont'd

Ref		
Desig	Description	Part Number
L5	INDUCTOR,SM,220nH CER,10%,1008	1256-2BR2200K
L9	INDUCTOR, SM, 10.0uH, 10%,1812	1255-4G10000K
L10	INDUCTOR,SM,150nH CER,10%,1008	1256-2BR1500K
L11	INDUCTOR,SM,180nH CER,10%,1008	1256-2BR1800K
L12	INDUCTOR,SM,150nH CER,10%,1008	1256-2BR1500K
M1	IC, AD831AP, MIXER-L/D, PLCC20	2360-83100P20
РСВ	PCB, FRONT END AMP/MIX.,LB VHF	4310-72500411
01	TRANSISTOR DOOG 25 DND COT22	2120-BC807025
Q1 Q2	TRANSISTOR, BC807-25,PNP,SOT23 TRANSISTOR, NE46134, RF,SOT-89	2127-NE461340
	, , , , , , , , , , , , , , , , , ,	
R1	RES., SM, 1K00 0805, 1%,100ppm	1150-3A1001FP
R2	RES., SM, 332R 0805, 1%,100ppm	1150-2A3320FP
R3	RES., SM, 30R1 1206, 1%,100ppm	1150-1B30R1FP
R4	RES., SM, 3K01 0805, 1%,100ppm	1150-3A3011FP
R5	RES., SM, 475R 0805, 1%,100ppm	1150-2A4750FP
R6	RES., SM, 4R75 1206, 1%,400ppm	1150-0B4R75FG
R7	RES., SM, 49R9 0805, 1%,100ppm	1150-1A49R9FP
R8	RES., SM, 5K11 1206, 1%,100ppm	1150-3B5111FP
R9	RES., SM, 5K11 1206, 1%,100ppm	1150-3B5111FP
R11	RES., SM, ZERO OHM JUMPER,0805	1150-0A0R0000
R13	RES., SM, 121R 1206, 1%,100ppm	1150-2B1210FP
R14	RES., SM, 49R9 1206, 1%,100ppm	1150-1B49R9FP
R15	RES., SM, 121R 1206, 1%,100ppm	1150-2B1210FP
R16	RES., SM, 49R9 0805, 1%,100ppm	1150-1A49R9FP

BANDPASS FILTER BOARD ELECTRICAL PARTS LIST

Ref Desig	Description	Part Number)-FE3H035)-FE3H045	
			A10FF	A10-	
C1	CAP., SM, 68pF CER., 0805, C0G CAP., SM, 82pF CER., 0805, C0G	1008-1A680J1G 1008-1A820J1G		•	
C2	CAP., SM, 10pF CER., 0805, C0G	1008-1A100J1G		•	
	CAP., SM, 15pF CER., 0805, C0G	1008-1A150J1G	•		
C3	CAP., SM, 56pF CER., 0805, C0G	1008-1A560J1G		•	
	CAP., SM, 68pF CER., 0805, C0G	1008-1A680J1G	•		
C4	CAP., SM, 12pF CER., 0805, C0G	1008-1A120J1G	•		
	CAP., SM, 8.2pF CER., 0805,C0G	1008-0A829J1G	\perp	•	
C5	CAP., SM, 68pF CER., 0805, C0G	1008-1A680J1G		•	
	CAP., SM, 82pF CER., 0805, C0G	1008-1A820J1G	•		
C6	CAP., SM, 12pF CER., 0805, C0G	1008-1A120J1G	•		
	CAP., SM, 8.2pF CER., 0805,C0G	1008-0A829J1G		•	
C7	CAP., SM, 56pF CER., 0805, C0G	1008-1A560J1G		•	
	CAP., SM, 68pF CER., 0805, C0G	1008-1A680J1G	•		
C8	CAP., SM, 10pF CER., 0805, C0G	1008-1A100J1G		•	
	CAP., SM, 15pF CER., 0805, C0G	1008-1A150J1G	•		
C9	CAP., SM, 68pF CER., 0805, C0G	1008-1A680J1G		•	
	CAP., SM, 82pF CER., 0805, C0G	1008-1A820J1G	•		
<u> </u>	INDUCT/VAR.,162-252uH,TAP3+3/8	1263-10C05U27		•	
	INDUCT/VAR.,193-306uH,TAP4+3/8	1263-10C06U35	•		
L2	INDUCTOR/VAR.,162-252nH,UNSHLD	1262-110C207U	•		
	INDUCTOR/VAR.,193-306nH,UNSHLD	1262-110C250U		•	
L3	INDUCTOR/VAR.,162-252nH,UNSHLD	1262-110C207U	•		
	INDUCTOR/VAR.,193-306nH,UNSHLD	1262-110C250U		•	
L4	INDUCTOR/VAR.,162-252nH,UNSHLD	1262-110C207U		•	
	INDUCTOR/VAR.,193-306nH,UNSHLD	1262-110C250U	•		
L5	INDUCT/VAR.,162-252uH,TAP3+3/8	1263-10C05U27		•	
	INDUCT/VAR.,193-306uH,TAP4+3/8	1263-10C06U35	•		
PCB	PCB, FRONT END/COIL MTG,LB VHF	4310-75500341	•	•	

MECHANICAL PARTS LIST

Description	Part Number	Qty
BRACKET, CAP. MTG,V/UHF 5P F/E	3702-65001005	1
		_
CABLE, CONFORM., 30mm/8mm/8mm	7489-C1070200	1
CABLE,SMB PLUG-OPEN,RG316,15CM	7910-WP007015	1
CABLE,SMB PLUG-OPEN,RG316,19CM	7910-WP008019	1
CABLE,SMB STR/PL-N(CSK)JK,21CM	7910-WS7NJ021	1
CASE, LB VHF, RF AMP/HELICAL	3702-65801050	1
CONN., SMB, JACK,BULKHEAD,REAR	5121-J1SC01BG	1
LOCKWASHER,M2.5,SPLIT,A2 STEEL	5814-2M5LK00S	18
NUT, PRESS,M2.5,5.6mmOD,PC MNT	5833-T2M55615	12
SCREW, M2.5 x 25 PAN/PHIL., A2	5812-2M5PP25S	18
SCREW, M3 X 6, FLAT/PHIL., A2	5812-3M0FP06S	4
SHIELD, V/UHF F/E AMP-LO DIVDR	3702-67301122	1
SHIELD, V/UHF F/E AMP-RF DIVDR	3702-67301123	1
SHIELD, V/UHF F/END AMP - WALL	3702-67301120	1
STANDOFF,5.56mmOD,4mmL,M2.5,SW	5927-5S4BM25T	6
WIRE, TFE/STRAND., 24AWG, RED	7121-24S19362	20cm



REVISION HISTORY

Revision	Date	ECO	Description
1	July 97		First Issue
1-0-1	Apr 2004		Updated document to new format